

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(completion queue) with ((patch or correction or fixing) near3 module) with ((end devices) or (hard disk drive) or HDD or disk or disc)	USPAT	ADJ	OFF	2007/06/13 15:46
L2	0	(completion queue) same ((patch or correction or fixing) near3 module) same ((end devices) or (hard disk drive) or HDD or disk or disc)	USPAT	ADJ	OFF	2007/06/13 15:47
L3	0	(complet\$4 queue) same ((patch or correction or fixing) near3 module) same ((end devices) or (hard disk drive) or HDD or disk or disc)	USPAT	ADJ	OFF	2007/06/13 15:47
L4	1	(complet\$4 queue) same ((patch or correction or fixing) near3 module) same ((end devices) or (hard disk drive) or HDD or disk or disc)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	OFF	2007/06/13 15:47
L5	1	(complet\$4 near3 queue) same ((patch or correction or fixing) near3 module) same ((end devices) or (hard disk drive) or HDD or disk or disc)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	OFF	2007/06/13 15:48
L6	1	queue same ((patch or correction or fixing) near3 module) same ((end devices) or (hard disk drive) or HDD or disk or disc)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	OFF	2007/06/13 15:48
L7	243	((patch or correction or fixing) near3 module) same ((end devices) or (hard disk drive) or HDD or disk or disc)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	OFF	2007/06/13 15:48
L8	154	I7 and (complet\$4 or queue)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	OFF	2007/06/13 15:49

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L9	2	I7 and (complet\$4 adj queue)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	OFF	2007/06/13 15:49
L10	0	I7 and (input adj queue)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	OFF	2007/06/13 15:50
L11	0	("6339801" "6345326").pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	ADJ	OFF	2007/06/13 15:50
L12	2	("6339801" "6345326").pn.	USPAT	OR	OFF	2007/06/13 15:53
L13	2	("6339801" "6345326").pn. and patch	USPAT	OR	OFF	2007/06/13 15:57
L14	3	("6859867" "5943482" "5594868").pn.	USPAT	OR	OFF	2007/06/13 16:00
L15	0	("6859867" "5943482" "5594868").pn. and complet\$4	USPAT	OR	OFF	2007/06/13 16:00
L16	3	("6859867" "5943482" "5594868").pn. and complet\$4	USPAT	OR	OFF	2007/06/13 16:01
L17	3	("6859867" "5943482" "5594868").pn. and (discard or "throw away")	USPAT	OR	OFF	2007/06/13 16:01

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discard "completion queue"

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Results 1 - 10 of about 704 for **discard "completion queue"**. (0.05 seconds)

Method and system for using a patch module to process non-posted ...

The system of claim 9, wherein the patch module can instruct the **completion queue** to **discard** a completion associated with the new cycle. ...

www.freepatentsonline.com/20050182869.html - 59k - [Cached](#) - [Similar pages](#)

Single instruction type based hardware patch controller - Patent ...

Discard completion When this flag is set, the **completion queue** is flag instructed to **discard** the completion associated with this request cycle so that the ...

www.freepatentsonline.com/20050223292.html - 52k - [Cached](#) - [Similar pages](#)

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Processor unit for a parallel processor system discards a received ...

If a **discard** instruction is issued, the whole of a received packet to be OS forms a transmission **completion queue**, stores the reception PU number and ...

www.patentstorm.us/patents/5594868-description.html - 78k - [Cached](#) - [Similar pages](#)

ATM cell conversion circuit and ATM cell conversion method - US ...

... frame into a sending **completion queue** within the frame buffer (step S20). ... Therefore, efficiency of buffer use can be improved and a **discard** rate of ...

www.patentstorm.us/patents/6944182-description.html - 48k - [Cached](#) - [Similar pages](#)

.TITLE TELSRV DCN/RT-11 Telnet service .SBTTL System and module ...

TTYOU BR ECHO ; ; Port 9 - sink (sin) tcp **discard** ("black hole") ; SINK: BLKW QUESIZ.*7. ;space for **completion queue** .EVEN ; .END TELSRV.

malarky.udel.edu/~dmills/data/du0/TELSRV.MAC - 14k - [Cached](#) - [Similar pages](#)

[PDF] Parallel pull-based LRU: a request distribution algorithm for ...

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frontend can then just **discard** the entry from the incoming. queue. It and the **completion queue** mechanisms reduce the synchro- nization by more than ...

ieeexplore.ieee.org/iel5/7358/19961/00923239.pdf - [Similar pages](#)

[PDF] Flexible and formal modeling of microprocessors with application ...

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Discard An OSM can **discard** a token. This transaction requires and a 6-entry **completion queue**. To faithfully model all these ...

ieeexplore.ieee.org/iel5/8443/26600/01253667.pdf?arnumber=1253667 - [Similar pages](#)

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[PDF] Parallel Pull-Based LRU : a Request Distribution Algorithm for ...

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frontend can then just **discard** the entry from the incoming the DSM: an index table and a **completion queue** that are. fixed in the frontend memory. ...

doi.ieeeecomputersociety.org/10.1109/CCGRID.2001.923239 - [Similar pages](#)

Method and system for using a patch module to process non-posted ...

The system includes a patch module coupled to a **completion queue** included in ... the **completion queue** whether or not to **discard** a completion received from a ...

www.patentdebate.com/PATAPP/20050182869 - Supplemental Result - [Similar pages](#)

Training - PowerPC MPC854X

... 12-entry instruction queue, 14-entry **completion queue**; Execution units : 2 CLR, CTD/CDV; Traffic policy; Traffic shaping; Early packet **discard** ...

www.mvd-fpga.com/en/formations002881A.html - 32k - [Cached](#) - [Similar pages](#)

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